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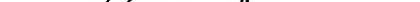
INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 2

Complete if Known

Application Number	10/720,297
Filing Date	November 24, 2003
First Named Inventor	Joe A. Ortiz
Art Unit	2838
Examiner Name	Patel, Rajnikant
Attorney Docket Number	PD-03W081

Examiner Signature		Date Considered	12/16/05
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***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²See Kind Codes of USPTO Patent Documents at www.uspto.gov or MPEP 801.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 2

of 2

	10/140,491
Filing Date	11/24/2003
First Named Inventor	Joe A. Ortiz
Art Unit	2838
Examiner Name	Patel, Rajnikant
Attorney Docket Number	PD-03W-81

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		RAMI, NAIM ET AL: "H Deg Deg Control of Boost Converters: Comparison to Voltage Mode, feedforward, and Current Mode Controls",	
		Record of the Annual Power Electronics Specialists Conference, 12 June 1995, pp 1327-1332, Vol. 2 conf. 26, IEEE, New York, NY, USA	
		REDL, R ET AL: "Optimizing the Load Transient Response of the Buck Converter", Applied Power Electronics Conference and Exposition,	
		15 February 1998, pp 170 - 176, Vol. 1, IEEE, New York, NY, USA	
		KANEMARU, S ET AL: "Analysis and Optimum Design of a Buck-Type DC-to DC Converter Employing Load current Feedforward", Power Electronics	
		Specialists Conference, 17 May 1998, pp 309-314, Vol. 1, IEEE, New York, NY, USA	

Examiner Signature	<i>Raj Patel</i>	Date Considered	11/10/05
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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